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	Application No.	Applicant(s)	
	09/879,824	BALAN ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Kimberly E Glenn	2817	
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. \square This communication is responsive to $\underline{6/1/04}$.			
2. The allowed claim(s) is/are 1-21,23-26,28-31 and 33-35.			
3. \boxtimes The drawings filed on <u>11 June 2001</u> are accepted by the E	xaminer.		
4.			
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 7. Examiner's Amendr 8. Examiner's Stateme 9. Other	(PTO-413), te ment/Comment	

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: With regards to claims 1-21, the prior art of record does not disclose or fairly teach the target voltage being set to be equal to 2Vth, where Vth is a threshold voltage of the output transistor. With regards to claims 23,24,28,29,33 and 34, the prior art of record does not disclose or fairly teach the first capacitor array having a capacitance twice as large as that of the unit capacitor of a conventional array having a total capacitance being the same at the first total capacitance. With regards to claims 25,26,30,31,35 and 36, the prior art of record does not disclose or fairly teach the second capacitor array having a capacitance twice as large as that of the unit capacitor of a conventional array having a total capacitance being the same at the second total capacitance.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimberly E Glenn whose telephone number is (571)-272-1761. The examiner can normally be reached on Monday-Friday 7:30 to 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571)-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kimberly E Glenn

Examiner

Art Unit 2817

keg

Supervisor Patent Examiner

Technology Center 2800